

PHYSICAL DESIGN INTERN

Uniquify Viet Nam One Member LLC

Level 14, Sai Gon Giai Phong Tower, ward 5, district 3, Ho Chi Minh city

Level 5, Indochina Riverside Tower building, 74 Bach Dang Street, Hai Chau 1 ward, Hai Chau district, Da Nang city

Work Location: **Ho Chi Minh**

Opportunities:

- We are seeking a sharp and highly motivated person whom our experienced ASIC team can teach and train into an excellent Physical Design Engineer.
- Candidate will work closely with experienced engineers developing standard IPs (DDR, ISP, ...) which are common in ASIC designs.
- Candidate will gain understanding and practical knowledge of design architecture, design flow. The internship will focus on the following but not limited to RTL coding, synthesis, simulation, static timing analysis, power analysis, as well as design integration.
- This is a great opportunity for anyone who wants to start a career in the ASIC design industry.

Qualifications:

- In the last year of undergraduate program in Electrical Engineering or Computer Engineering.
- Strong analytical and critical thinking skills.
- Good communication skills, positive attitude, and ability to work in a team environment.
- High GPA score (7.0 and above)
- Good understanding about Boolean Logic (AND, OR, NOR, NOT ..)
- Good understanding of Fundamentals of Synchronous logic design.
- Exposure to a scripting languages such as TCL, Perl, and/or Make is a plus.
- Coursework in digital IC design a plus.



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<http://www.uniquify.com>

Benefits:

- 6-month internship program, from basic to in-depth knowledge
- Competitive monthly salary
- International working environment with a professional, young & dynamic team
- Learn how to use EDA tools (Cadence, Synopsys, Mentor)
- Have an opportunity to work on real projects
- Possibly become a full-time employee after internship period.

How to apply:

Please send a package of your documents to email address unq_vn_hr@uniquify.com for screening:

- Resume in English
- Transcript
- TOEIC/ IELTS/ TOEFL certificate.