



## AMS TEAM – IC DESIGN INTERN

### 1. Opportunities:

- Be trained for fundamental knowledge of IC design in Circuit and Layout design.
- 5,700,000 VND monthly allowances
- Good interns will be converted to official Synopsys employees.
- Experience real working environment and enjoy team activities.
- Be trained for key soft skills as professional engineers.
- Support capstone: be provided interesting topics and guided by dedicated engineers.

### 2. Internship Descriptions:

- Stage 1: Fundamental Training (4 months)
  - Training for fundamental knowledge of IC design
  - Training for both Theory and Lab to understand the basic of key jobs of IC design (Digital, Circuit, Layout...)
  - All training results from interns will be reviewed and interns will be advised for the best suitable jobs in IC design.
- Stage 2: Mini project is used as graduate thesis (3 months)
  - Interns passed evaluation in stage 1 will be grouped into small groups, belonging to different capstone topic/university.
  - Mini project will contains all the basic requirement of a real project so that interns will be very familiar with the real jobs after this training: Task planning, weekly status reports, design gate reviews, evaluation...
  - Project results will be reviewed and interns with good evaluations will be considered for official job offer from Synopsys.

### 3. Skills Requirements:

- Final year students of Electronics Engineering, Electromechanics, Telecommunications, Automation.
- GPA > 3/4 or 7.0/10 is a plus (Latest updated)
- Basic knowledge of CMOS and AMS design flow
- Interested in IC design and desire to pursue long-term career path of IC design
- Commit to participate in at least 70% of the internship program
- Good English communication (Writing and reading)
- Active learning and learning strategies
- Be highly accountable, patient, hard-working, and easy-going, Good teamwork skills



## WORK WITH AMS

### Opportunities

- **5,700,000 VND** monthly allowances
- Opportunity to be converted to official Synopsys employees
- Capstone supported with interested topics and guided by senior engineers
- Enjoy AMS "play hard-work hard" culture with many activities (sport, camping,...)

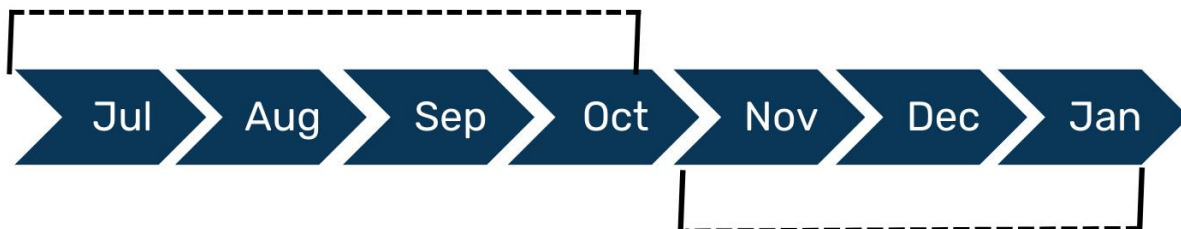
### Requirements

- Final year students
- GPA > 3/4 or 7.0/10
- Commit to participate in at least 70% of the program

### Descriptions

Stage 1: Fundamental Training (4 months)

- Fundamental knowledge of IC design (circuit/layout)
- Be reviewed and advised for the best suitable jobs



Stage 2: Mini project is used as graduate thesis (Optional)

- Contains all the basic requirement of a real project: Task planning, weekly status reports, design gate reviews, evaluation...

### Application

- Email: [vn-staffing@synopsys.com](mailto:vn-staffing@synopsys.com)
- Note: Send CV + Transcript by 23 June

