

Seminar Details

Title: **Artificial Intelligence Verification-The Future of Chip Verification**

Short Abstract:

Implementation of artificial intelligence (AI) in chip design verification is the futuristic vision of Truechip. The company has already automation and GUI tools which today is capable of automatic test bench generation, automatic environment creation and regression analysis along with performance measurement.

AI coupled with Truechip's automation tool will revolutionize the design verification. AI will bring more automation in verification. We will have superior testing and verification methods which will use the past data and use AI-ML algorithms to get robust coverage. It will also reduce time-to-market by automation and rapid impact analysis of different implementations.

Speaker Biography:

Speaker: **Suyash Jain**

Over 12+ years of design engineering experience in the semiconductors industry. In his career Suyash has successfully developed many complex SoC Verification IPs and also successfully led SoC Verification efforts in various SoC silicon tape outs. His key skills include SOC Verification, SOC Boot / Secure Boot - Powerup & Resets, NOC , DDRx , LPDDRx , HBM, PCIe ,UCle ,Die2Die Communication, QSPI ,eMMC & I3C. Suyash has Bachelor of Engineering Degree from Jiwaji University Gwalior India , Master of Technology (MTech) focused in VLSI from Rajiv Gandhi Proudyogiki Vishwavidyalaya, Bhopal India. & PG Diploma in VLSI Design from CDAC ACTS Pune India.

Photo:



Duration: **35 minutes**